

PATENT

Attorney Docket No. 100-14300 [P04927]

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE APPLICATION FOR UNITED STATES PATENT

In re Patent Application of) Group Art Unit: 2823
Terry Lines) Examiner: D. Collins
Appln. No. 09/824,653) REVOCATION OF POWER OF ATTORNEY BY
Filed: April 3, 2001) ASSIGNEE AND APPOINTMENT OF NEW) COUNSEL AND CHANGE OF ADDRESS
For: METHOD FOR FORMING A DEPLETION- MODE TRANSISTOR THAT ELIMINATES THE NEED TO SEPARATELY SET THE THRESHOLD VOLTAGE OF THE DEPLETION-MODE TRANSISTOR)))))))
	-

Commissioner for Patents Washington, D.C. 20231

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right, title and interest in the above-identified application by assignment dated March 27, 2001, hereby revokes all previous powers of attorney in connection with the above-identified application.

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right, title and interest in the above-identified application by assignment dated March 27, 2001, which assignment is recorded in the U.S. Patent and Trademark Office at Reel 011669, Frame 0476, hereby appoints the firm of Law Offices of Mark C. Pickering, a firm composed of: Mark C. Pickering (36,239); and the following members of the National Semiconductor Corporation Intellectual Property Department: Christopher J. Byrne (32,204); Eugene C. Conser (39,149); John Maxin (34,668); Coleman F. Reif (38,593); Allen R. Tremain (40,207); Andrew S. Viger (28,552); and Peter Y. Wang (40,452) as our attorney(s) or agent(s) to prosecute the above-identified application, and to transact all business in the U.S. Patent and Trademark Office connected therewith.

Please change the correspondence address for the above-identified application and direct all future correspondence to the following address:

Mark C. Pickering Law Offices of Mark C. Pickering P.O. Box 300 Petaluma, California 94953-0300 Direct Telephone: (707) 762-5583 Main Telephone: (707) 762-5500

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NATIONAL SEMICONDUCTOR CORPORATION

Dated: 7/16/07

By: Name:

Christopher J. Byrne

Title:

Director of Intellectual Property and Technology Licensing



PATENT

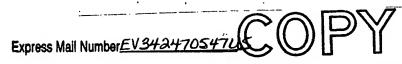
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE APPLICATION FOR UNITED STATES PATENT

In re Patent Application of	Group Art Unit: 2815
Terry Lines)	Examiner: Eugene Lee
Appln. No. 09/824,653	ESTABLISHMENT OF RIGHT OF ASSIGNEE TO PROSECUTE UNDER 37 C.F.R. §3.73(b)
Filed: April 3, 2001	TO TROSECUTE ORDER S7 C.T.R. 93.73(b)
For: DEPLETION-MODE TRANSISTOR THAT) ELIMINATES THE NEED TO) SEPARATELY SET THE THRESHOLD) VOLTAGE OF THE DEPLETION-MODE) TRANSISTOR (as amended))	
Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450	
Sir:	
and interest in the above-identified application	RATION is the assignee of the entire right, title on by assignment dated March 27, 2001, which

Frame 0476.

•	The ur	ndersigned	l (whose ti	tle is supp	olied below)	is empowered	to sign this	s statement
on beha	If of th	ne assigne	e.			1.0	_	
Dated:	8	13/13		_ By:		N)		
) - 0		Name:	Christopher	J. Byrne		
				Title:	Director of	Intellectual Pro	perty and T	echnology
					Licensing			

NATIONAL SEMICONDUCTOR CORPORATION



Atty Docket No. 072219-0276257 [P04927]

DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

believe I am the original, first a inventor (if plural names are list on the invention entitled	ind sole inventor (if only on- ed below) of the subject ma	e name is listed below) or an origi atter which is claimed and for which	nal, first and joint ch a patent is sought
		STOR THAT ELIMINATES THE E DEPLETION-MODE TRANSIS	
the specification of which (chec was amended on (if applica		eto or was filed on as App	lication No and
I hereby state that I have review the claims, as amended by any		ntents of the above-identified specove.	cification, including
I acknowledge the duty to discl	ose all information which is	material to patentability as define	ed in 37 CFR § 1.56.
patent or inventor's certificate, country other than the United S	or § 365(a) of any PCT Inte	19(a)-(d) or § 365(b) of any foreig emational application which designerals and foreignerals and foreignat of the application on which pri	nated at least one n application for
Number	Country	Day/Month/Year Filed	_ X
I hereby claim the benefit und	er 35 U.S.C. § 119(e) of an	/ United States provisional applic	ation(s) below.
Application Number	Filing Date		
Application Number	Filing Date		
International application design the claims of this application first paragraph of 35 U.S.C.	gnating the United States, liss not disclosed in the prior 112, I acknowledge the du CFR § 1.56 which became	United States application(s), or § 3 sted below and, insofar as the sulunited States application in the material ty to disclose all information which available between the filing date oplication:	bject matter of each of nanner provided by the h is material to
Application Number	Filing Date	Status: Patented, Pendi	ng, Abandoned
Application Number	Filing Date	Status: Patented, Pendi	ng, Abandoned

PATENT Atty Docket No. 072219-0276257 [P04927]

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor TERRY LINES		
Inventor's signature Inventor's signature	3/26/2001	
	Date	
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